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STATEMENT UNDER 37 CFR 3.73(b)				
Applicant/Patent Owner: Emerson Network Power - Embedded Computing, Inc.				
Application No./Patent No.:	10/671204	Filed/Issue Date:	September 24, 2003	
Entitled: Method Of Early Buffer Return				
Emerson Network Power - Embedded Computing, Inc.  (Name of Assignee)  , a Corporation  (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)				
states that it is:				
1. x the assignee of the entire right, title, and interest; or				
2. an assignee of less than the entire right, title and interest.				
(The extent (by percentage) of its ownership interest is %)				
in the patent application/patent identified above by virtue of either:				
A. An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel				
B. x A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:  1. From: Wise, Jeffrey L., et al.  To: Motorola, Inc.				
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Additional documents in the chain of title are listed on a supplemental sheet.				
As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.				
[NOTE: A separate copy ( <i>i.e.</i> , a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]				
The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.				
/Joseph M. Lafata/ July 8, 2008				
Signature			Date	
Joseph M. Lafata			(248) 641-1223	
Printed or Typed Name Telephone Number			Telephone Number	
Attorney/Autho	rized to Sign for Assignee Title			